

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. – 11. (Canceled)

12. (currently amended) A clock control system for generating a clock signal having an operating frequency set to a nominal operating frequency corresponding to a maximum rated junction temperature, comprising:

a thermal sensor operative to produce a temperature signal corresponding to a junction temperature of at least a portion of a circuit on a die;

a thermal sensor control circuit, operatively coupled to the thermal sensor, and operative to produce temperature data in response to the temperature signal and to provide a control signal in response to the temperature data;

a clock generator circuit operative to produce the clock signal; and

a dynamic overclock frequency control data generator, operatively coupled to the thermal sensor control circuit ~~and operative to receive the temperature data, and operatively coupled to~~ and the clock generator circuit, and operative to provide dynamic overclock frequency control data to the clock generator circuit in response to the control signal and the received temperature data to cause the clock generator circuit to increase the operating frequency of the clock signal above the nominal operating frequency, when the detected junction temperature is less than the maximum rated junction temperature.

13. (currently amended) The clock control system of claim 12 wherein the dynamic overlock frequency control data generator is operative, in response to the control signal, to provide hysteresis based frequency control to increase the operating frequency of the clock signal above the nominal operating frequency if the detected junction temperature is below a lower junction temperature threshold, and to decrease the temperature dependent dynamic overlock generator circuit decreases the operating frequency of the clock signal below the nominal operating frequency if the detected junction temperature is above an upper junction temperature threshold, wherein the upper junction temperature threshold is greater than the lower junction temperature threshold.

14. (canceled)

15. (currently amended) The clock control system of claim ~~[[14]]~~12 wherein the thermal sensor control circuit ~~interrupt control circuit~~ is operative to produce the ~~interrupt~~ control signal in response to a comparison between the temperature data and the threshold temperature data, ~~such that the dynamic overlock frequency control data generator is operative to produce the dynamic overlock frequency control data in response to the interrupt control signal.~~

16. (original) The clock control system of claim 12 wherein the dynamic overlock frequency control data generator is operative to reduce at least one of: the frequency of the clock signal and a supply voltage to at least the portion of the circuit on the die in response to the control signal and if the first junction temperature is above a junction temperature threshold.

17. (currently amended) In a system comprising a host processor and a graphics co-processor, a method for generating a clock signal for the graphics co-processor, the clock signal having an operating frequency set to a nominal operating frequency corresponding to a maximum rated junction temperature, the method comprising:

detecting, by a thermal sensor coupled to the graphics co-processor, a junction temperature corresponding to at least a portion of a circuit on a die constituting at least a portion of the graphics co-processor, thereby providing a temperature signal;

providing, by a thermal sensor control circuit coupled to the thermal sensor, a control signal and temperature data in response to the temperature signal; and

causing, by the host processor coupled to the thermal sensor control circuit and in response to the control signal and the temperature data, an increase in increasing the operating frequency of the clock signal above the nominal operating frequency, when the detected junction temperature is below the maximum rated junction temperature.

18. (currently amended) The method of claim [[18]]17 further including decreasing, by the host processor in response to the control signal, the operating frequency of the clock signal below the nominal operating frequency when the detected junction temperature is below the maximum rated junction temperature.

19. (original) The method of claim 18 further including providing hysteresis based frequency control by:

decreasing, by the host processor in response to the control signal, the operating frequency of the clock signal if the detected junction temperature is above an upper junction temperature threshold, and

increasing, by the host processor in response to the control signal, the operating frequency of the clock signal if the detected junction temperature is below a lower junction temperature threshold wherein the upper junction temperature threshold is greater than the lower junction temperature threshold.

20. – 33. (Canceled)